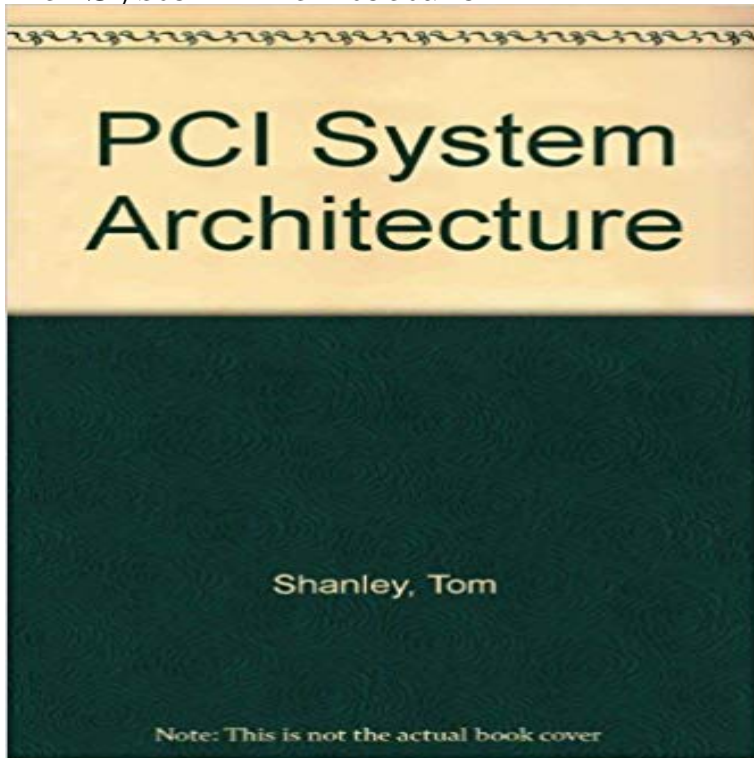


# Pci System Architecture



Fourth Edition now available! One of the major benefits of the PCI specification is that it is defined with many engineering disciplines in mind: architecture, protocol, components, board layout, and software. Mindshare's PCI System Architecture is an excellent resource for understanding all these aspects and their interrelationships. -Todd Koelling, Applications Engineer, Intel Corporation; member of the PCI development team

PCI System Architecture describes revision 2.1 of the Peripheral Component Interconnect (PCI) bus specification, providing a clear, concise explanation of PCI's relationship to the rest of the system. This book has been updated and revised to include in-depth treatment of PCI to PCI bridges, the PCI BIOS, the 66MHz PCI bus, and more. PCI experts Tom Shanley and Don Anderson provide a comprehensive treatment of the bus including:

- \* reflected-wave switching
- \* shared resource acquisition
- \* signal groups
- \* 64-bit extension
- \* bus arbitration
- \* add-in cards and connectors
- \* command set
- \* configuration
- \* read and write transfers
- \* expansion ROMs
- \* premature transaction termination
- \* cacheable memory support

This book also examines the VLSI Technology VL82C59x chipset to illustrate an example PCI bus implementation. If you design or test hardware or software that involves the PCI bus, PCI System Architecture is an essential, time-saving tool. The PCI System Architecture Series is a crisply written and comprehensive set of guides to the most important PC hardware standards. Each title explains from a programmer's perspective the architecture, features, and operations of systems built using one particular type of chip or hardware specification.

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PCI System Architecture is a detailed and comprehensive guide to the Peripheral Component Interconnect (PCI) Bus

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