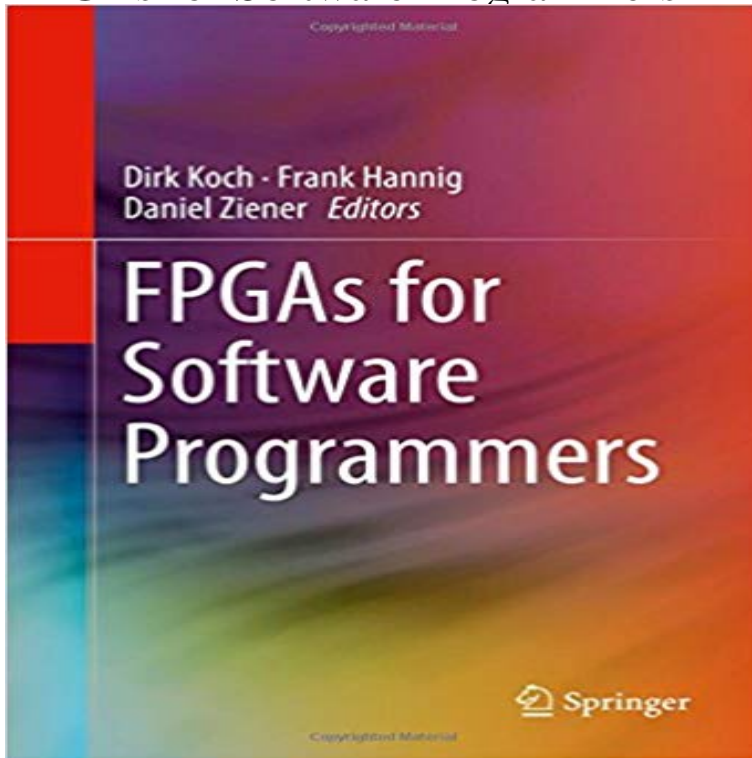


FPGAs for Software Programmers



This book makes powerful Field Programmable Gate Array (FPGA) and reconfigurable technology accessible to software engineers by covering different state-of-the-art high-level synthesis approaches (e.g., OpenCL and several C-to-gates compilers). It introduces FPGA technology, its programming model, and how various applications can be implemented on FPGAs without going through low-level hardware design phases. Readers will get a realistic sense for problems that are suited for FPGAs and how to implement them from a software designers point of view. The authors demonstrate that FPGAs and their programming model reflect the needs of stream processing problems much better than traditional CPU or GPU architectures, making them well-suited for a wide variety of systems, from embedded systems performing sensor processing to large setups for Big Data number crunching. This book serves as an invaluable tool for software designers and FPGA design engineers who are interested in high design productivity through behavioural synthesis, domain-specific compilation, and FPGA overlays. Introduces FPGA technology to software developers by giving an overview of FPGA programming models and design tools, as well as various application examples; Provides a holistic analysis of the topic and enables developers to tackle the architectural needs for Big Data processing with FPGAs; Explains the reasons for the energy efficiency and performance benefits of FPGA processing; Provides a user-oriented approach and a sense for where and how to apply FPGA technology.

This book makes powerful Field Programmable Gate Array (FPGA) and reconfigurable technology accessible to software engineers by covering different As well discuss, the hottest topic in the FPGA world is making FPGA programming more accessible to software developers. Intel is leading theThis book makes powerful Field Programmable Gate Array (FPGA) and reconfigurable technology accessible to software engineers by covering differentFirst well start

with how HDL synthesis is different from software programming. Imagine that every program, whether in hardware (on FPGA) or software is

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FPGAs for Software Programmers (FSP 2016). August 29, 2016, Lausanne, Switzerland co-located with. Interna onal Conference on Field Programmable Logic

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The FSP Workshop aims at bringing researchers and experts from both academia and industry together to discuss and exchange the latest research advances

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Invited Talk: LegUp High-Level Synthesis of Processor/Accelerator FPGA Systems P2: A Software Parallel Programming Approach to FPGA-Accelerated

The aim of this workshop is to make FPGA and reconfigurable technology accessible to software programmers. Despite their frequently proven power and

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